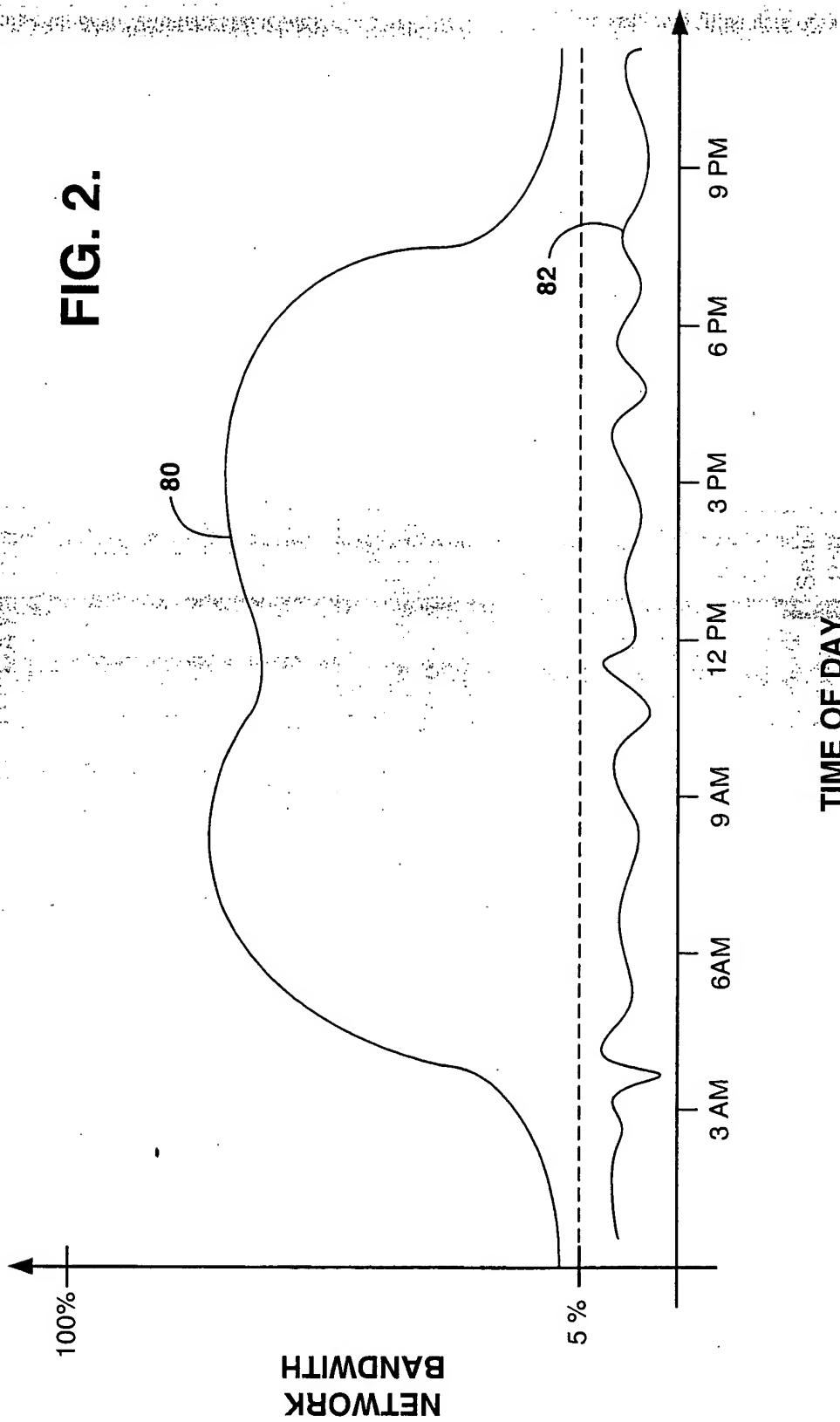


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FIG. 2.



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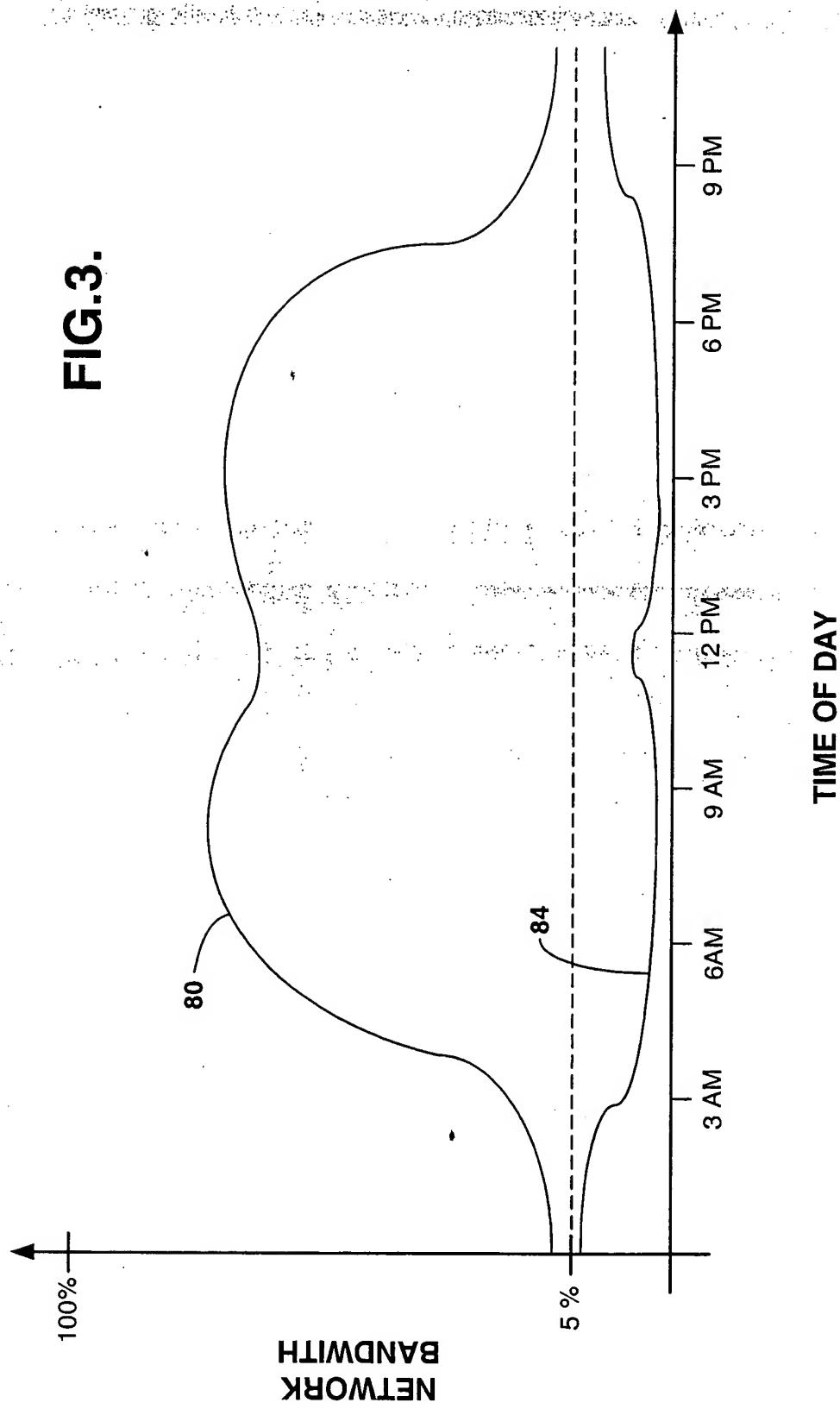


FIG.4.

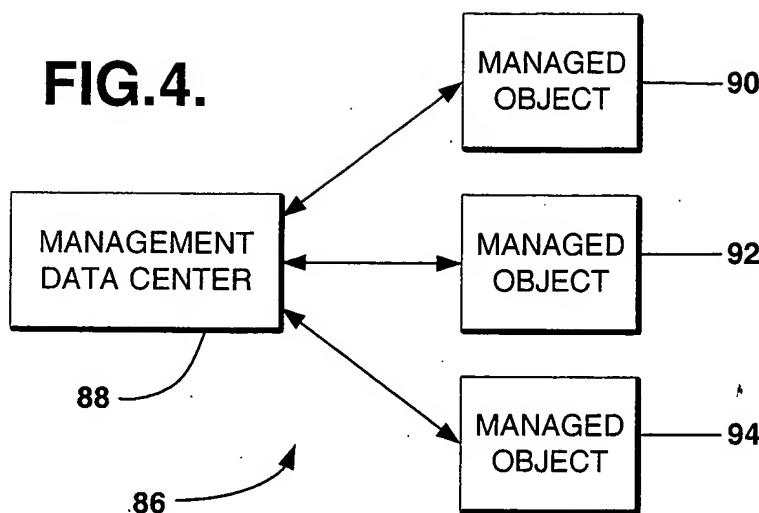


FIG.6.

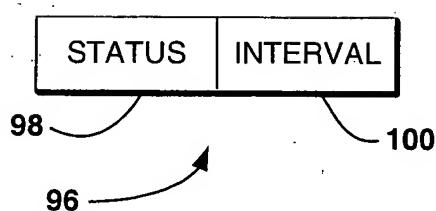
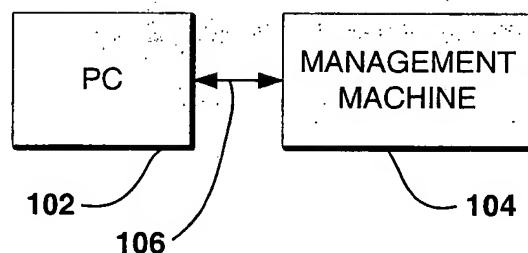


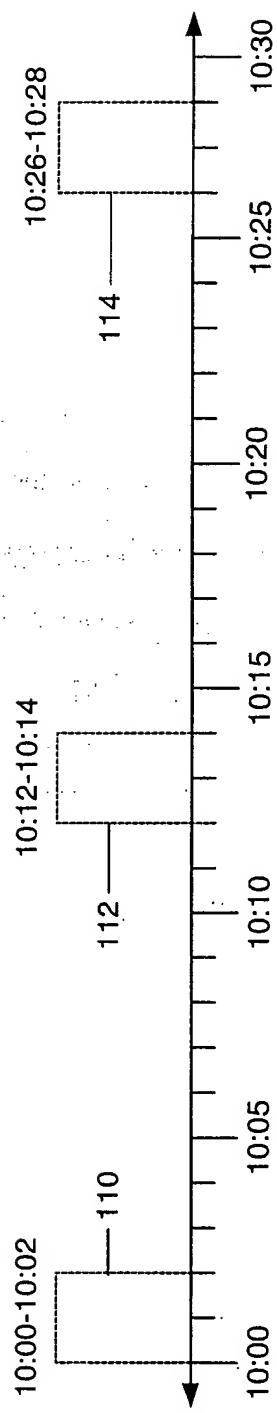
FIG.5.



TIME	STATUS	INTERVAL
10:01	OK	12
10:13	OK	14
10:27	OK	9
...
...
...

FIG.7.

FIG. 8.



BEST AVAILABLE CIRCUIT

FIG.9.

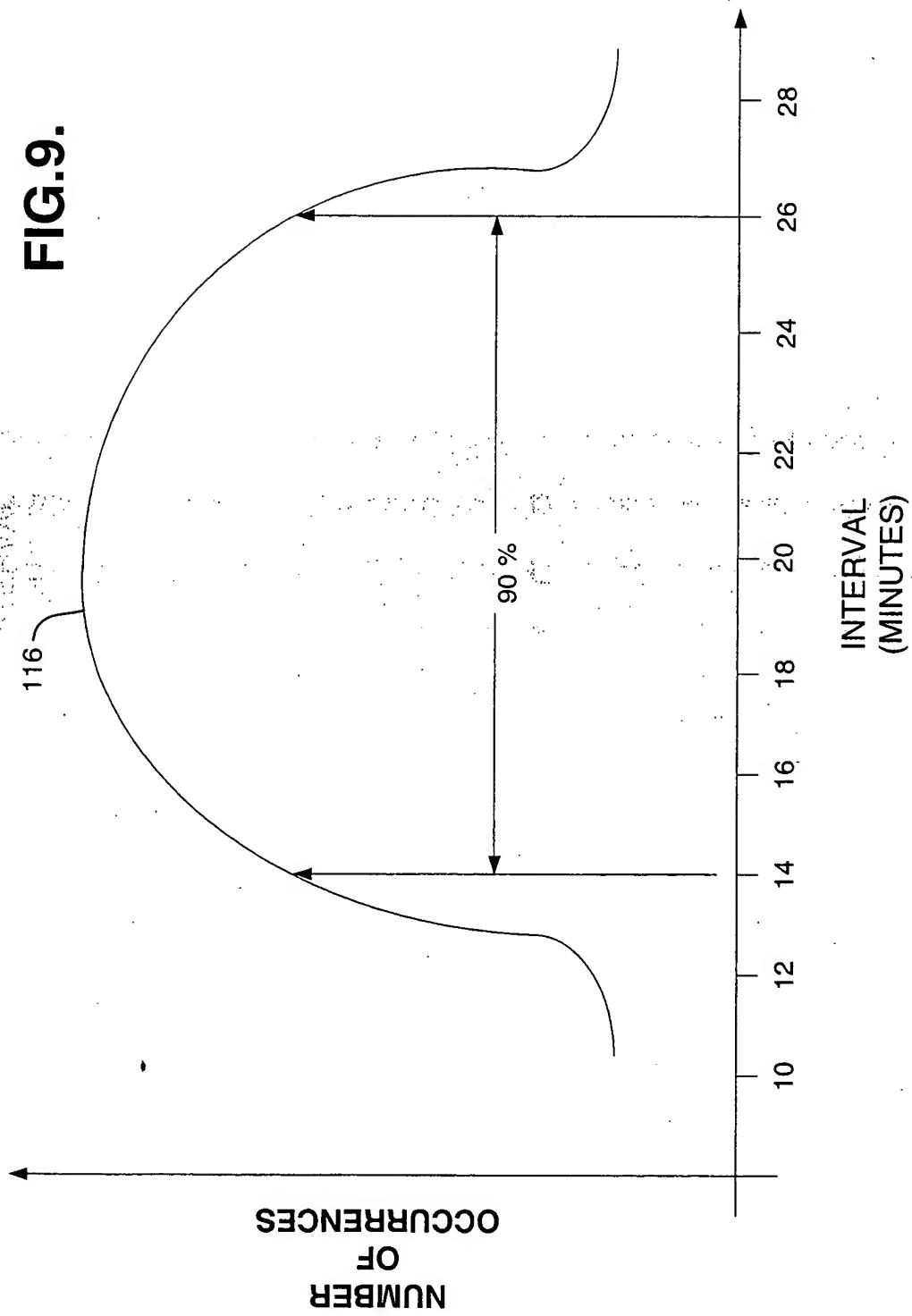


FIG.10.

